

Monday 20.01	Tuesday 21.01	Wednesday 22.01	Thursday 23.01	Friday 24.01
<b>Kick-Off Meeting Day</b>	<b>Coding and Wireless Day</b>	<b>Information Processing and Machine Learning Day</b>	<b>Design, Development and Implementation Day</b>	<b>Wrapping Up &amp; Mini-Projects Schedule</b>
<b>09:00 – 10:30</b> Twinning projects: <ul style="list-style-type: none"> <li>▪ Basic Facts</li> <li>▪ Reporting</li> <li>▪ Exploitation &amp; Communication</li> <li>▪ Legal Issues</li> <li>▪ Ethical Issues</li> <li>▪ Financial Issues</li> </ul>	<b>09:00 – 09:30</b> Research Overview AAU <b>09:30 – 10:10 (Coding)</b> <ul style="list-style-type: none"> <li>▪ Gianluigi Liva</li> <li>▪ Aleksandar Minja</li> </ul> <b>10:10 – 10:40 Coffee break</b> <b>10:40 – 12:00 (Coding)</b> <ul style="list-style-type: none"> <li>▪ Alex Graell i Amat</li> <li>▪ Mladen Kovacevic</li> <li>▪ Jelena Bjelica</li> </ul>	<b>09:00 – 09:30</b> Research Overview CHALMERS <b>09:30 – 10:10 (InfProc &amp; ML)</b> <ul style="list-style-type: none"> <li>▪ Dragana Bajovic</li> <li>▪ Vukan Ninkovic</li> </ul> <b>10:10 – 10:40 Coffee break</b> <b>10:40 – 12:00 (InfProc &amp; ML)</b> <ul style="list-style-type: none"> <li>▪ Mirsad Cosovic</li> <li>▪ Ognjen Kundacina</li> <li>▪ Dejan Vukobratovic</li> </ul>	<b>09:00 – 09:30</b> Research Overview DLR <b>09:30 – 10:10 (Wireless implementation)</b> <ul style="list-style-type: none"> <li>▪ Marcel Grec</li> <li>▪ Tijana Devaja</li> </ul> <b>10:10 – 10:40 Coffee break</b> <b>10:40 – 12:00 (Wirel. Implem.)</b> <ul style="list-style-type: none"> <li>▪ Ivan Mezei</li> <li>▪ Milan Lukic</li> <li>▪ Srdjan Sobot</li> </ul>	<b>09:00 – 10:00</b> Open Stage for Open Topics for Research Collaboration <b>10:00 – 10:30 Coffee break</b> <b>10:30 – 12:00</b> Mini-projects and research visits scheduling  Concluding Remarks and Goodbye!
<b>10:30 – 11:00</b> <b>Coffee Break</b>	<b>12:00 – 13:00</b> <b>Lunch</b>	<b>12:00 – 13:00</b> <b>Lunch</b>	<b>12:00 – 13:00</b> <b>Lunch</b>	<b>12:00 – 13:00</b> <b>Lunch</b>
<b>11:00 – 11:15</b> Skype Chat with P.O. <b>11:15 – 11:30</b> Research Overview ICONIC <b>11:30 – 12:00</b> Overview of INCOMING <b>12:00 – 12:20</b> WP 2 overview <b>12:20 – 12:40</b> WP 3 overview <b>12:40 – 13:00</b> Schools & Exp. Training	<b>13:00 – 14:00</b> <b>Keynote: Prof. Branka Vucetic</b>  <b>14:00 – 16:00 (Wireless)</b> <ul style="list-style-type: none"> <li>▪ Beatriz Soret</li> <li>▪ Federico Clazzer</li> <li>▪ Canan Aydogdu</li> <li>▪ Ceda Stefanovic</li> <li>▪ Milica Petkovic</li> </ul>	<b>13:00 – 15:00 (InfProc &amp; ML)</b> <ul style="list-style-type: none"> <li>▪ Tatjana Loncar-Turukalo /Niksa Jakovljevic/Ivan Lazic</li> <li>▪ Milos Kudus/Vladimir Petrovic</li> <li>▪ Milos Radosavljevic</li> <li>▪ Stanisa Dautovic/Natasa Samardzic</li> <li>▪ Nikola Gavric/Zivko Bojovic</li> </ul>	<b>13:00 – 14:00 (InfProc &amp; ML implementation)</b> <ul style="list-style-type: none"> <li>▪ Dragisa Miskovic</li> <li>▪ Rastislav Struharik/Predrag Teodorovic/Vuk Vranjkovic</li> </ul> <b>14:00 – 15:00</b> Open Stage for Open Topics for Research Collaboration	
<b>13:00 – 14:30</b> <b>Lunch Break</b>		<b>19:00 – 21:00</b> <b>Social Event</b>		
<b>14:30 – 14:50</b> WP 4 overview <b>14:50 – 15:10</b> WP 5 overview <b>15:10 – 15:30</b> WP 6 overview <b>15:30 – 16:00</b> Workshops & 5G Seminars <b>16:00 Closing Remarks</b>				

## **BRAINSTORMING WEEK TALKS:**

### **Tuesday (Coding Session):**

- Gianluigi Liva (DLR): “Multi-Edge Type LDPC Code Design for the McEliece Cryptosystem”
- Mladen Kovacevic (FTN-ICONIC): “Error Correction Under Worst Case Criteria”
- Alexandre Graell i Amat (CHALMERS): “Coding for distributed computing”
- Aleksandar Minja (FTN-ICONIC): “On Coding for URLLC and VLC”
- Jelena Bjelica (FTN-ICONIC): “Coded Slotted ALOHA with Multiple User Classes”

### **Tuesday keynote talk**

Prof. Branka Vucetic (USYD): “5G IoT Networks”

### **Tuesday (Wireless Session):**

- Beatriz Soret (AAU): “LEO small-satellite constellations in 5G and beyond”
- Federico Clazzer (DLR): “Space Diversity in Random Access Systems”
- Canan Aydogdu (CHALMERS): “Communications and Sensing Convergence”
- Cedimir Stefanovic (AAU): “Next Generation Radio Access Networks”
- Milica Petkovic (FTN-ICONIC): “Free-Space Optical (FSO) Wireless Communications”

### **Wednesday (Information Processing & Machine Learning Session 1)**

- Dragana Bajovic (FTN-ICONIC): “Network Based Information Processing”
- Vukan Ninkovic (FTN-ICONIC): “Wireless Receiver Design using Deep Neural Networks”
- Mirsad Cosovic (FTN-ICONIC): “Graphical Models in Power Systems”
- Ognjen Kundacina (FTN-ICONIC): “Reinforcement Learning for Dynamic Spectrum Access”
- Dejan Vukobratovic (FTN-ICONIC): “Research Topics in VLC and Smart Illumination”

### **Wednesday (Information Processing & Machine Learning Session 2)**

- Tatjana Loncar-Turukalo, Niksa Jakovljevic, Ivan Lazic (FTN-ICONIC): “Research Topics in Personalized and Predictive Medicine”
- Milos Radosavljevic, Branko Brkljac (FTN-ICONIC): “High bit-depth seismic data and multispectral satellite data compression”
- Milos Kudus, Vladimir Petrovic (FTN-ICONIC): “Imaging Workflow Automation Using CNN Based AI”
- Stanisa Dautovic, Natasa Samardzic (FTN-ICONIC): “In-situ Information Processing using Memristors and other Electrical Elements with Non-Volatile Memory”
- Nikola Gavric (FTN-ICONIC): “Denial of Service Attacks in Massive-Scale Gaming”

### **Thursday (Wireless Implementation Session)**

- Marcel Grec (DLR): “Aspects of SDR Implementation”
- Tijana Devaja (FTN-ICONIC): “Coded Random Access Implementation Using GNU Radio”
- Milan Lukic (FTN-ICONIC): “ICONIC-VIP Mobile NB-IoT Testbed and Use Cases”
- Ivan Mezei (FTN-ICONIC): “Federated number recognition for smart metering applications using NB-IoT wireless technology”
- Srdjan Sobot (FTN-ICONIC): “OAI-based 4G LTE Testbed”

### **Thursday (Information Processing Implementation Session)**

- Dragisa Miskovic (FTN-ICONIC): “Distributed computation testbed using stacked Raspberry Pi 4”
- Vuk Vranjkovic, Rastislav Struharik, Predrag Teodorovic (FTN-ICONIC): “Customized Hardware Architectures for Acceleration of Machine Learning Algorithms”